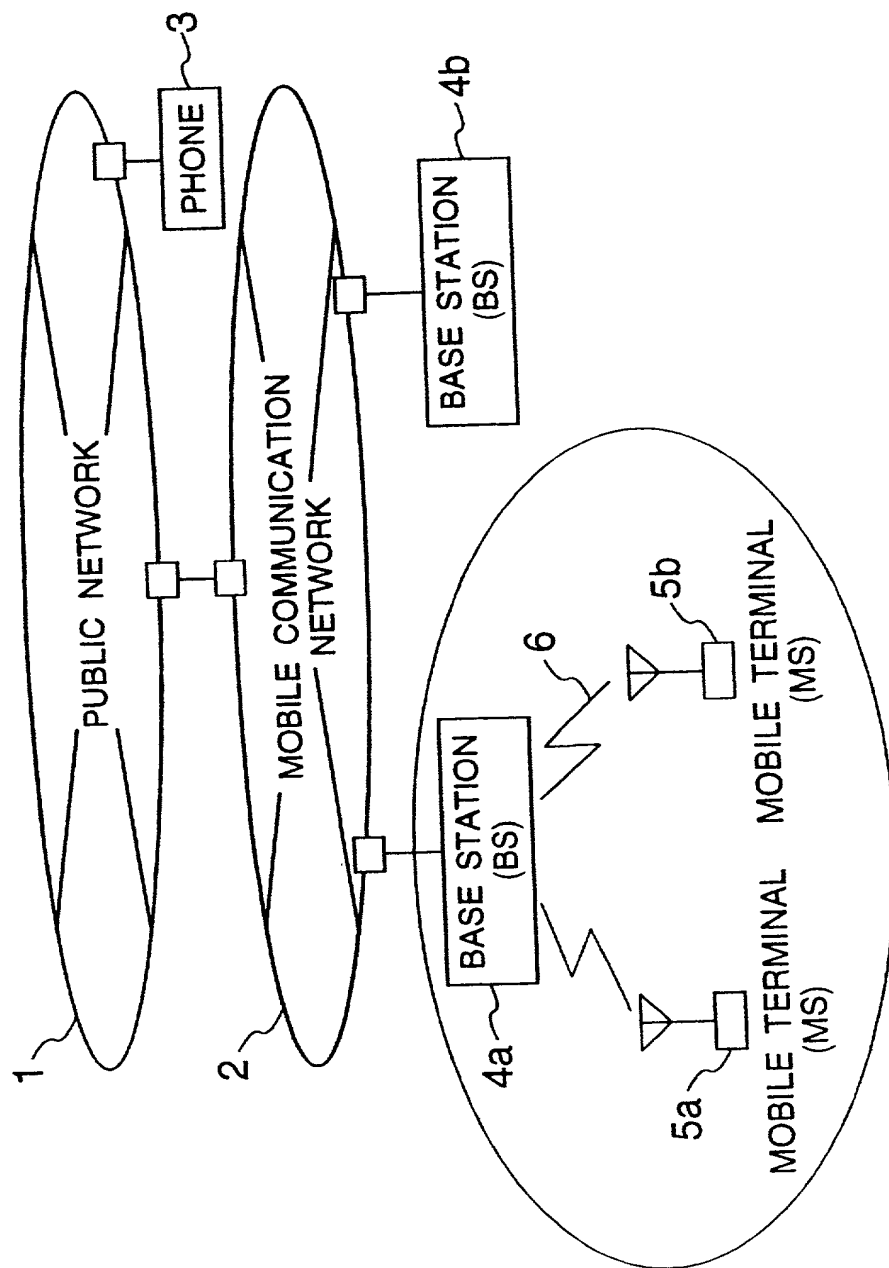
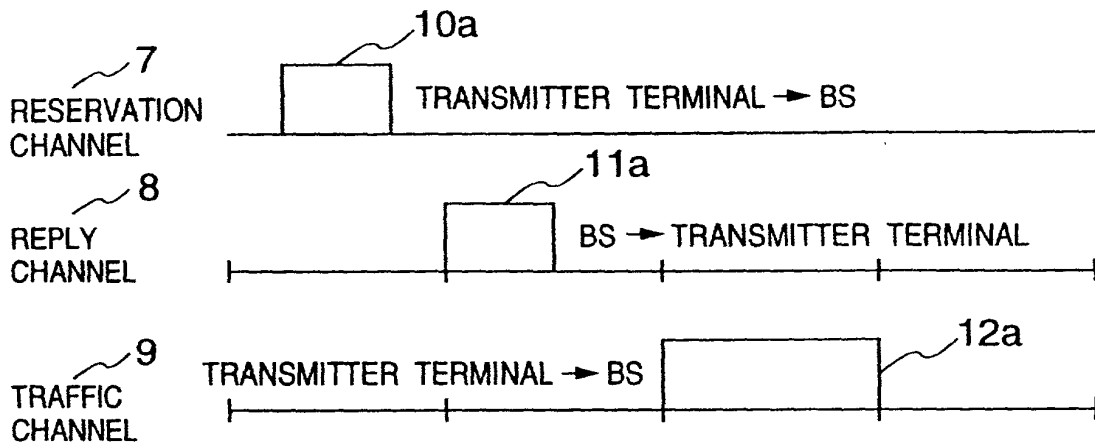


FIG.1



# FIG.2A



# FIG.2B

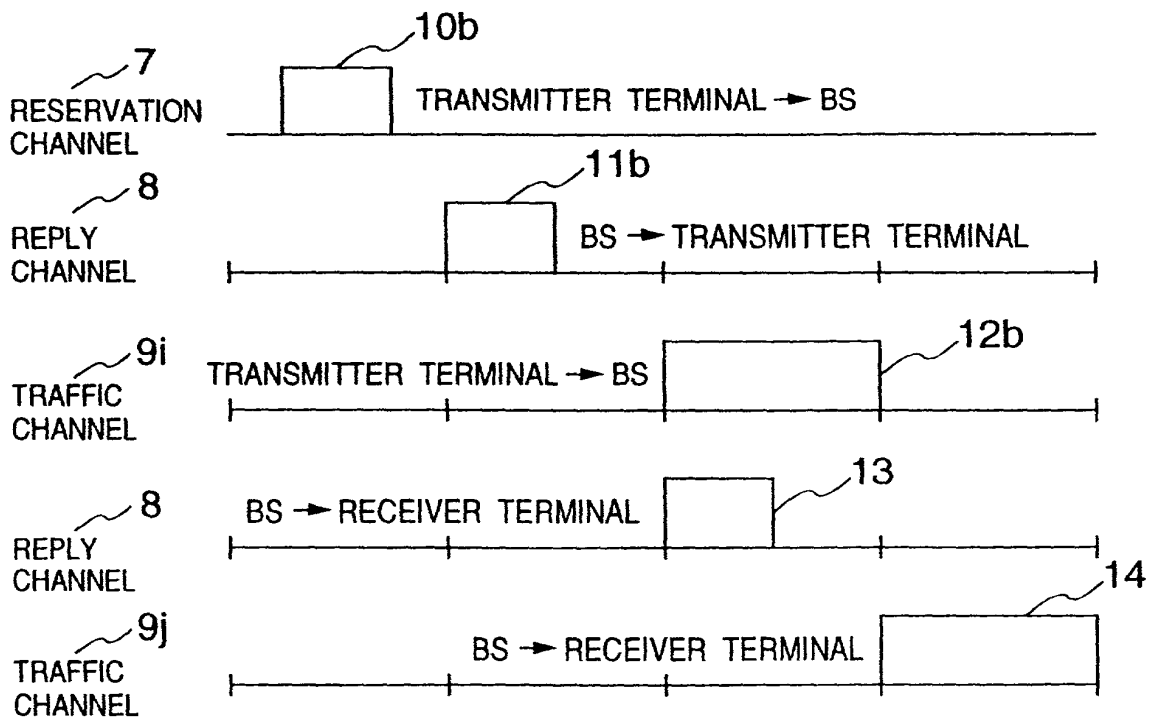


FIG.3

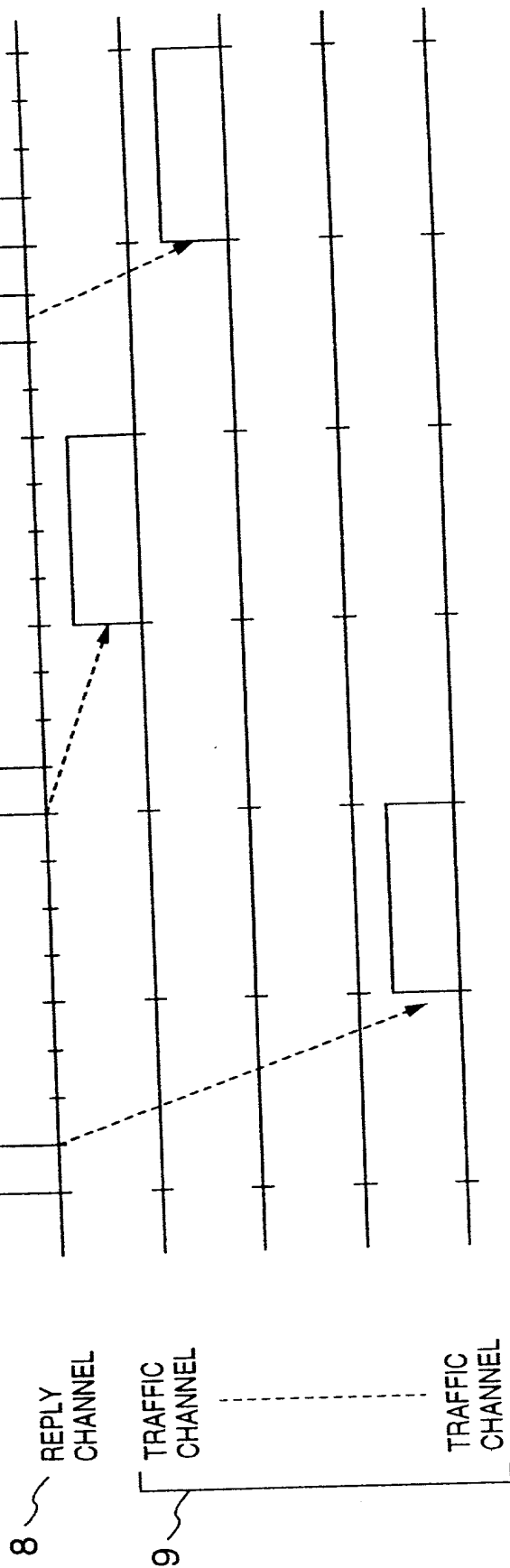
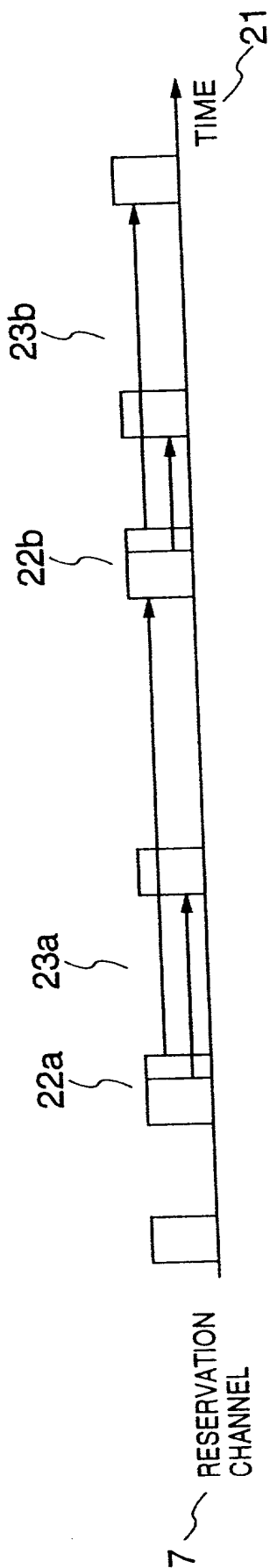


FIG. 4

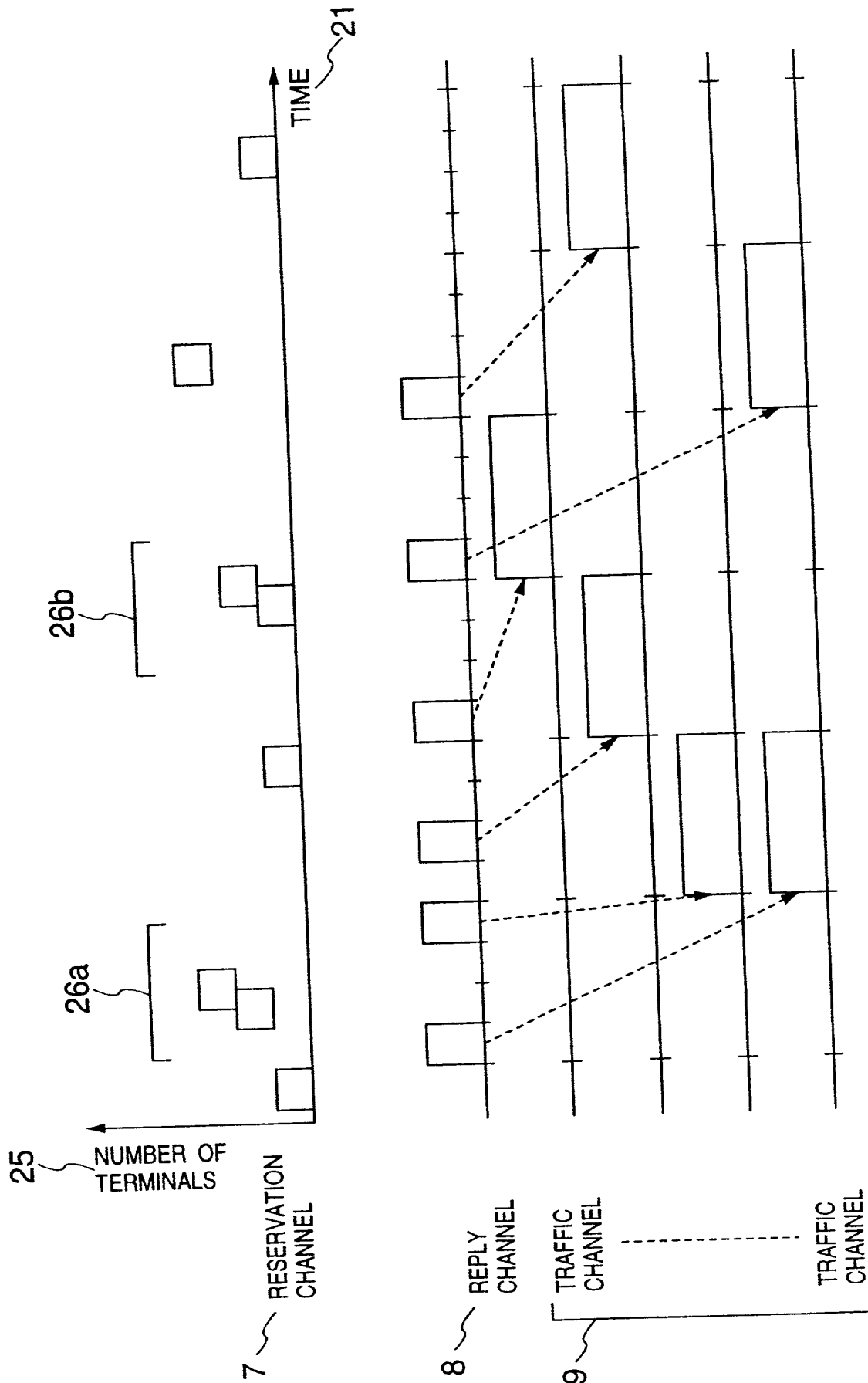


FIG. 5A

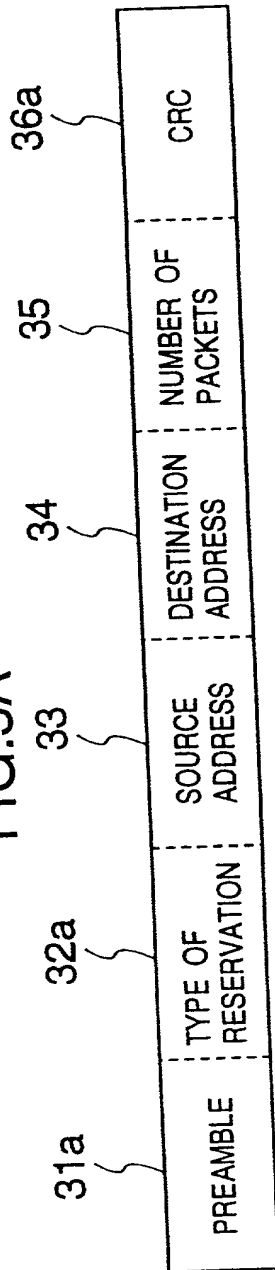


FIG. 5B

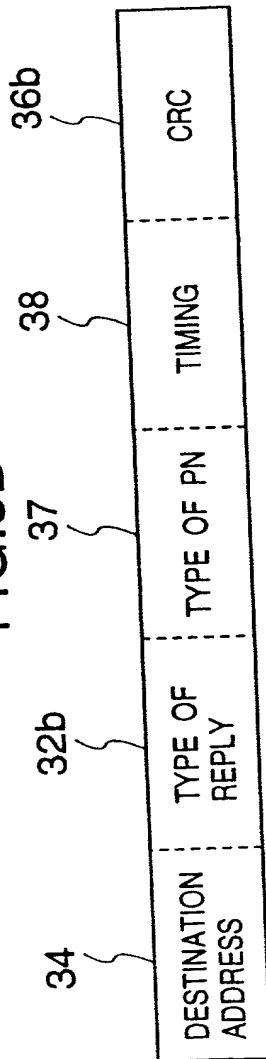


FIG. 5C

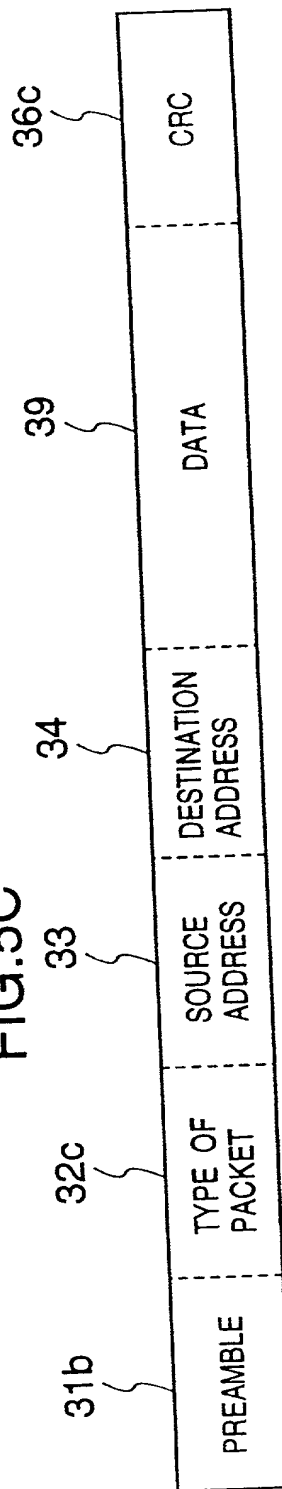


FIG. 6

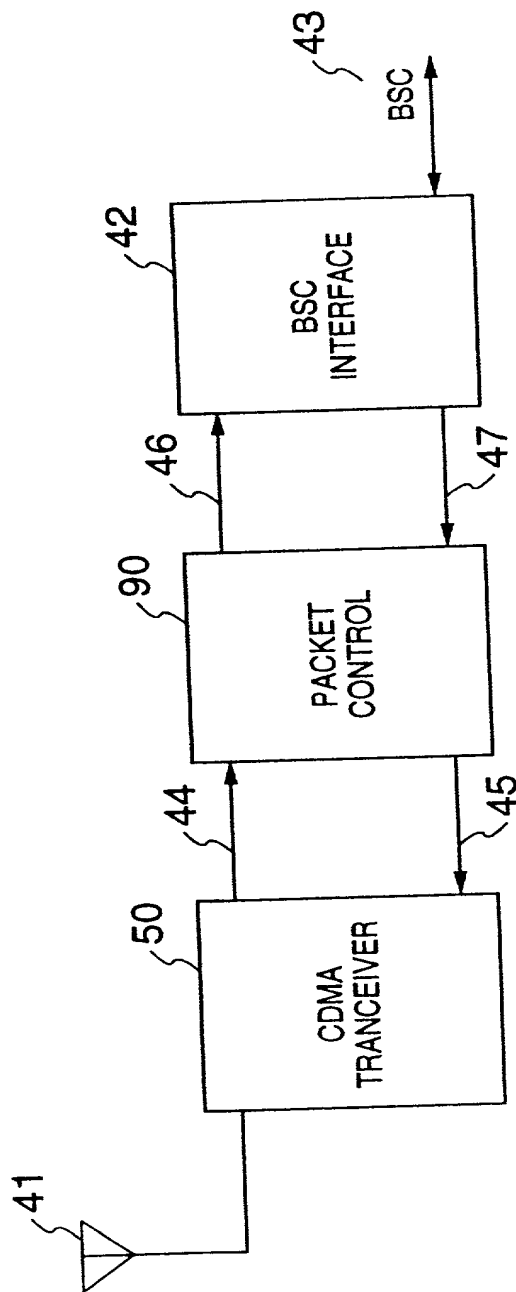
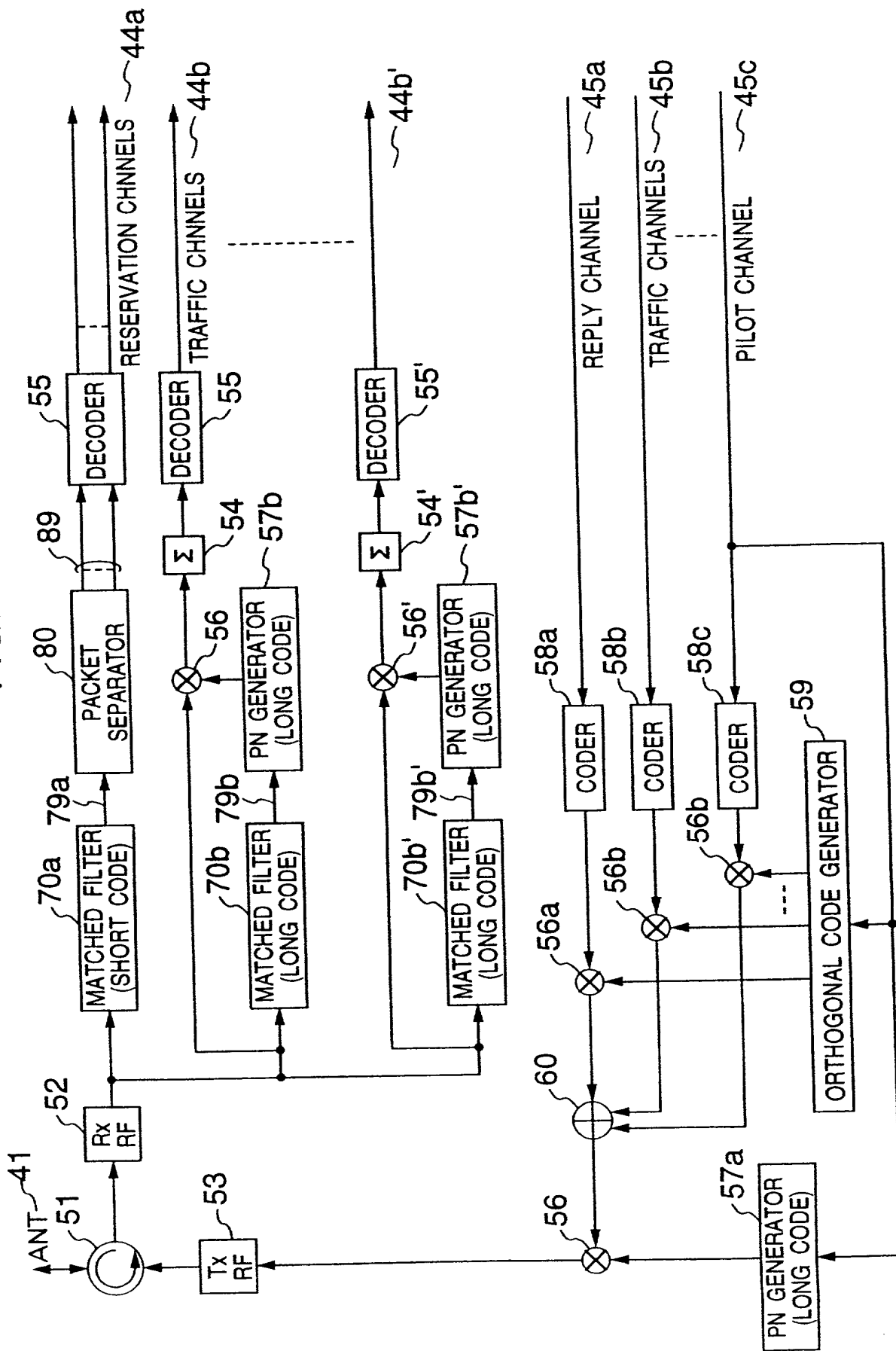
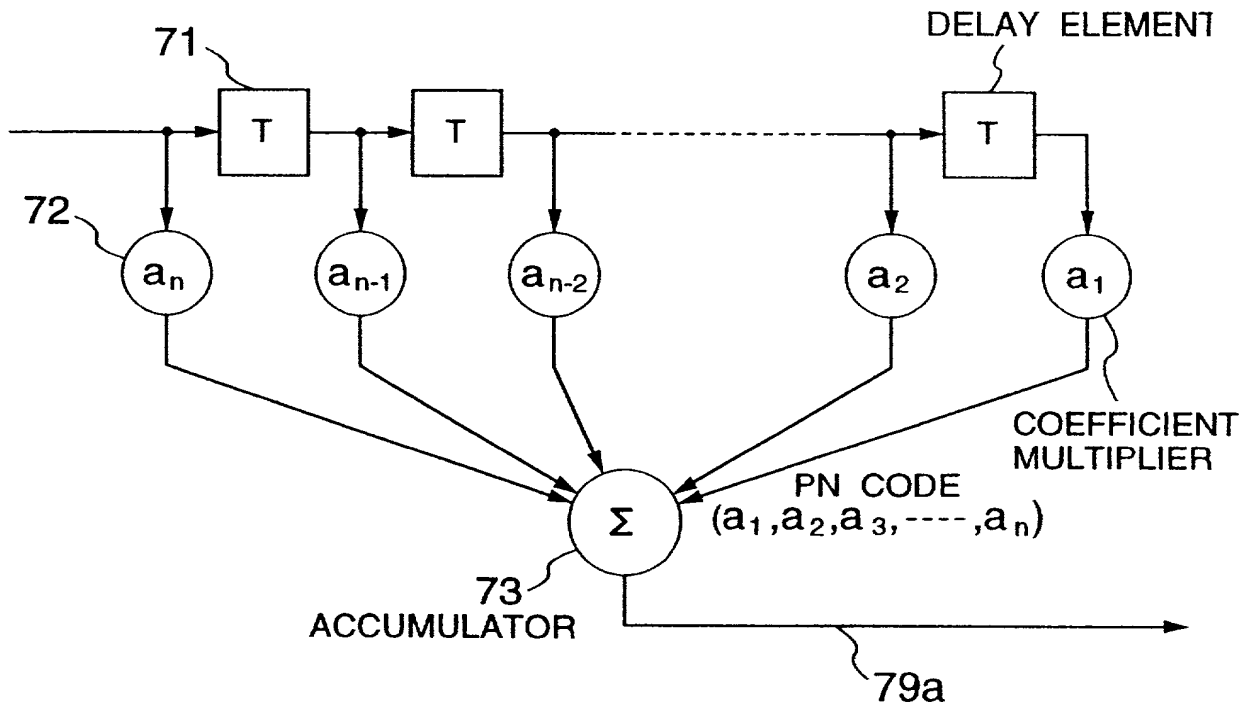


FIG. 7



# FIG.8A



# FIG.8B

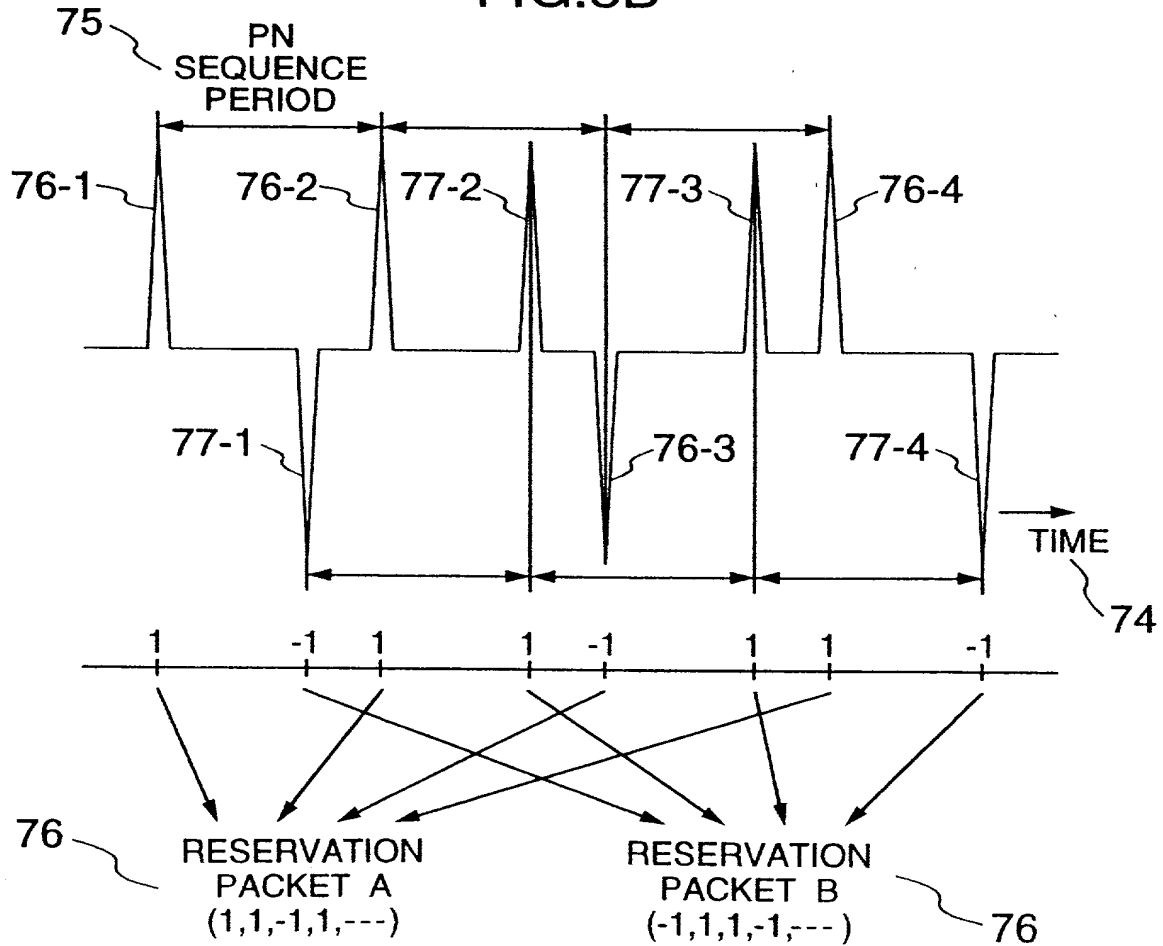
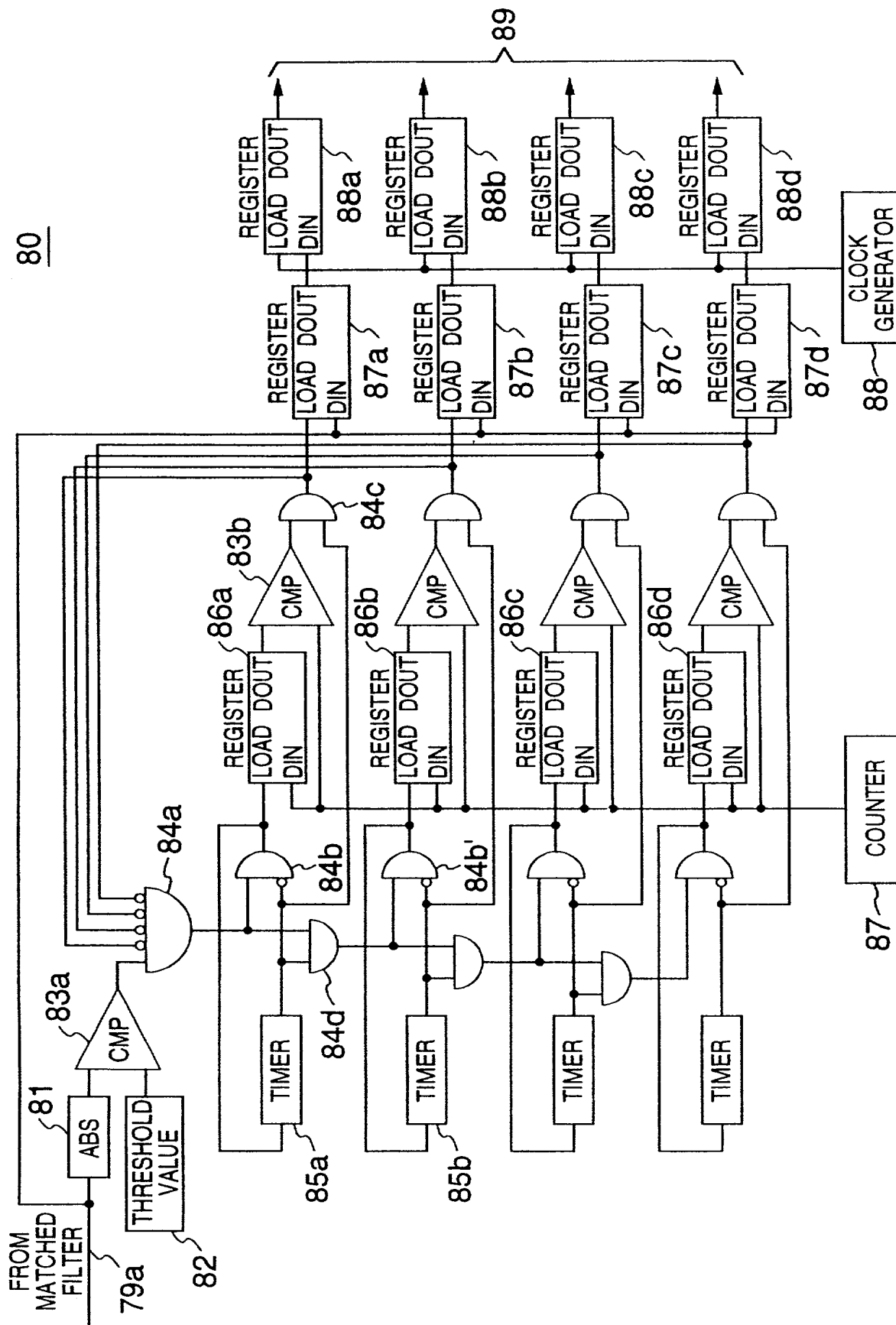




FIG. 9



DIGITAL SIGNAL PROCESSOR 91 FIG.10

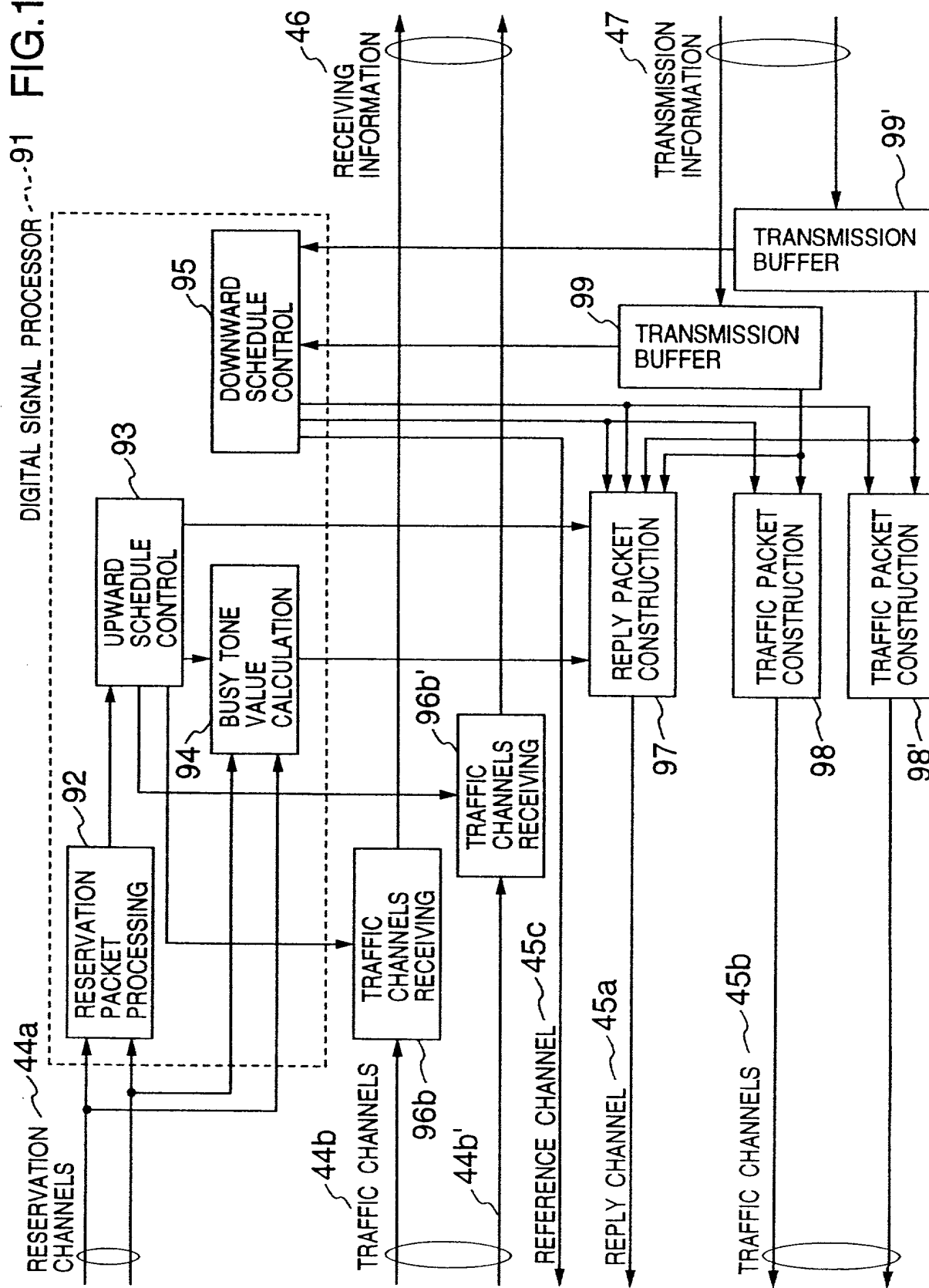


FIG. 11

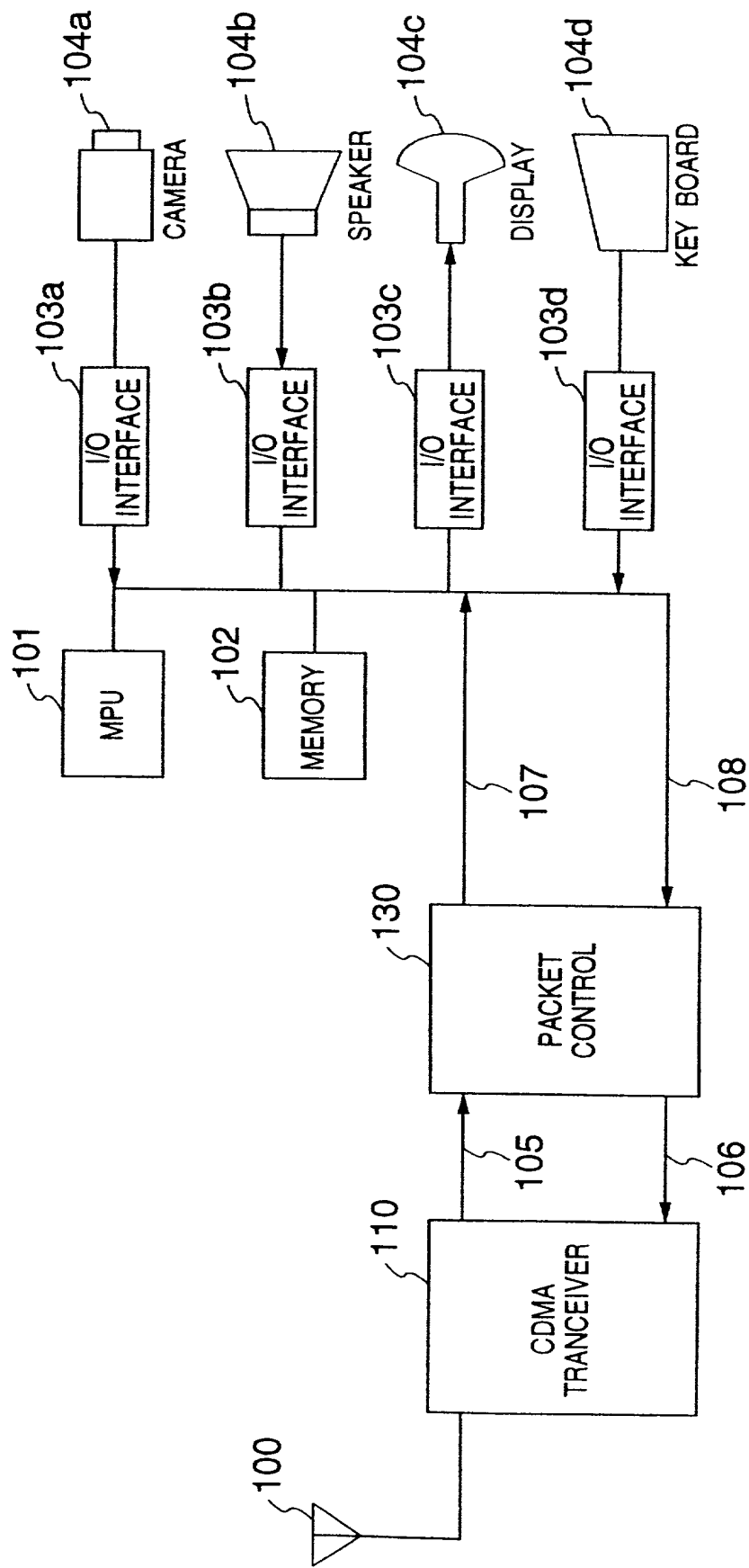


FIG.12

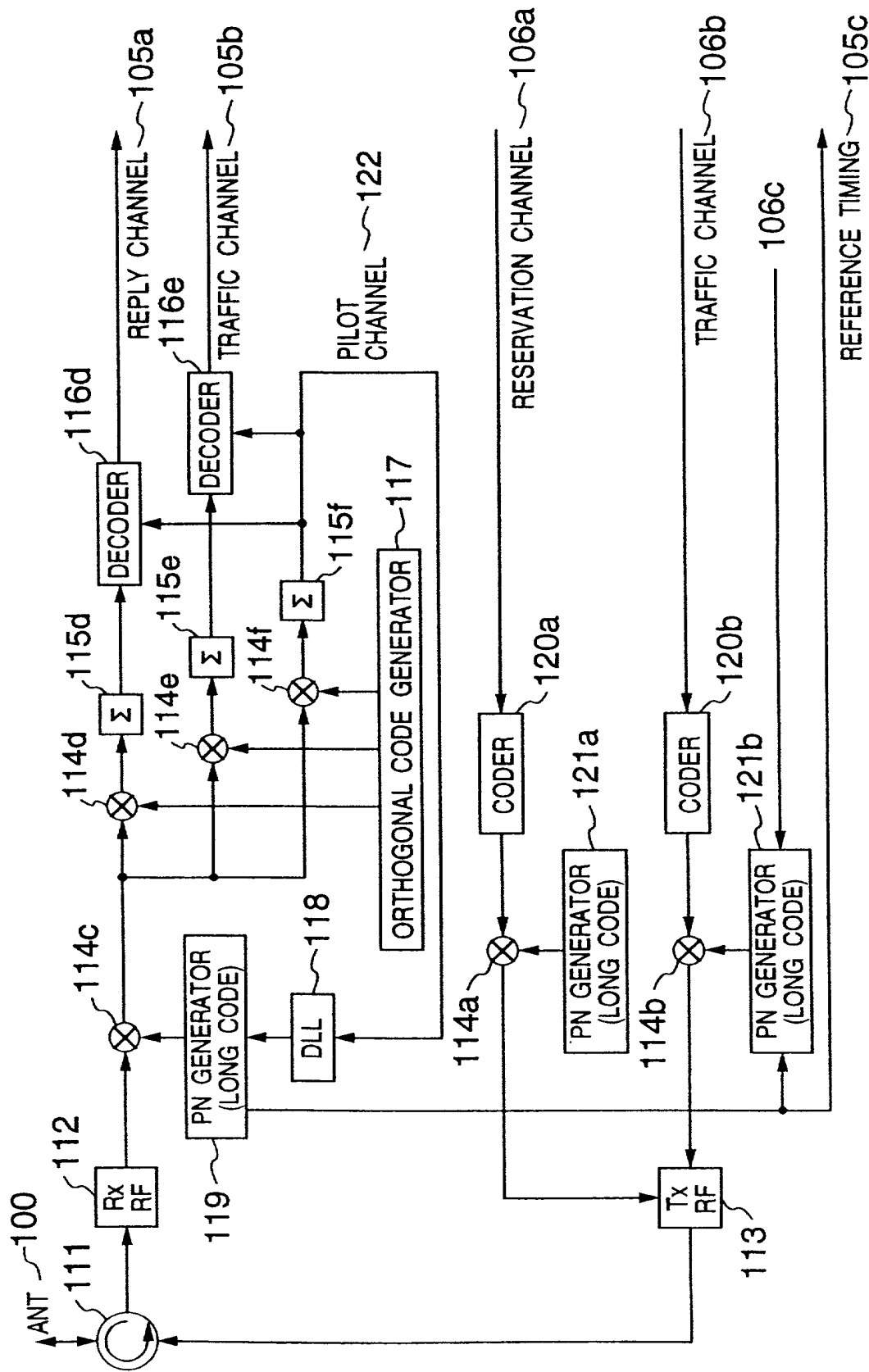


FIG. 13

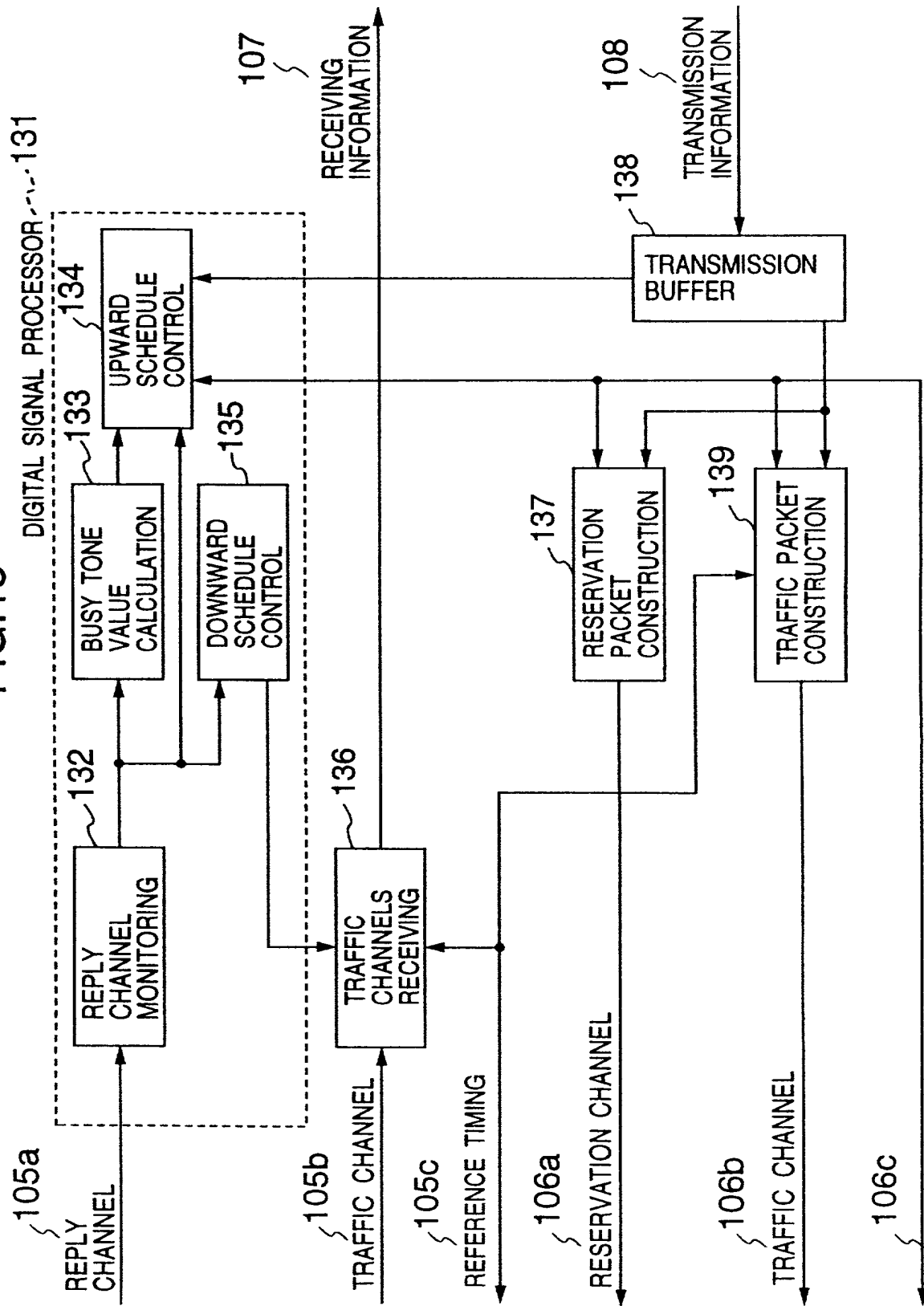


FIG.14A

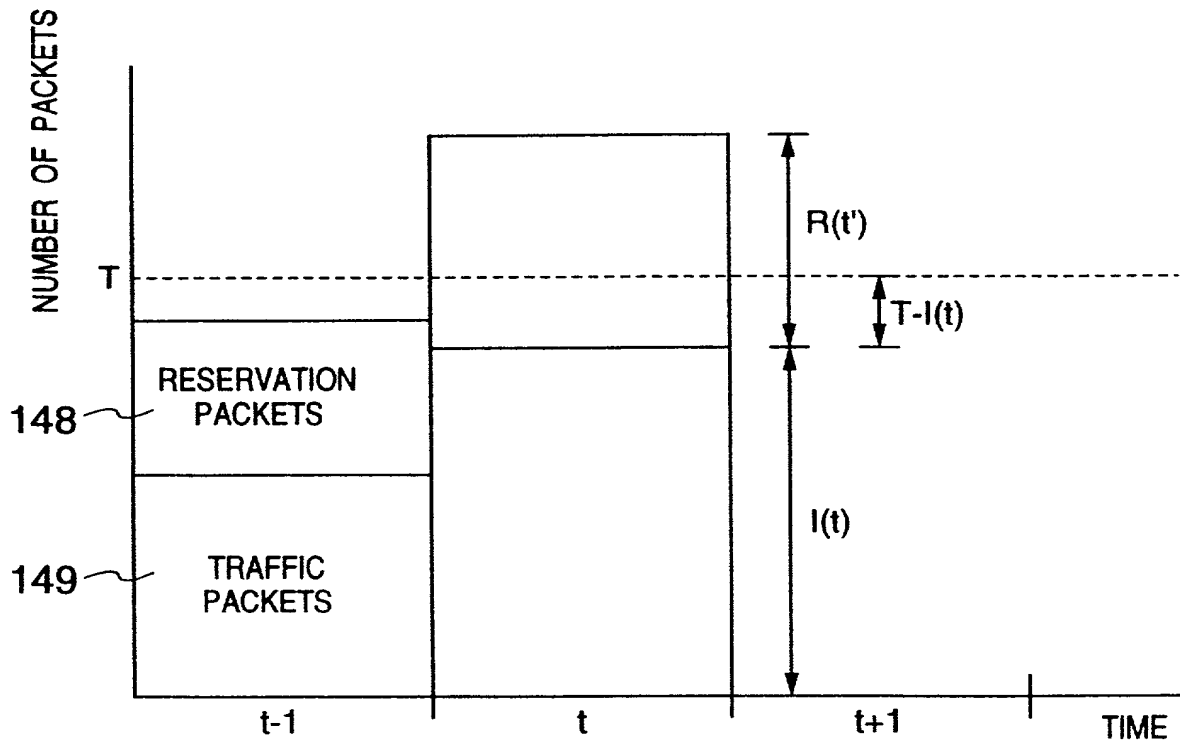


FIG.14B

